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CONFIRMATION NO. 5852

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/612,033	07/03/2003	257	2826	03180.0326
RULE				
APPLICANTS Tadashi Iguchi, Mie-ken, JAPAN; Katsuhiro Ishida, Mie-ken, JAPAN; Hiroaki Tsunoda, Mie-ken, JAPAN; Hirohisa Iizuka, Mie-ken, JAPAN; Hiroaki Hazama, Tokyo, JAPAN; Seiichi Mori, Tokyo, JAPAN; <div style="text-align: right;">[KQ]</div>				
** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** JAPAN P2002-338452 11/21/2002 <div style="text-align: right;">[KQ]</div>				
** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/26/2003				
Foreign Priority claimed	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS
35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	Initials	JAPAN	13
Verified and Acknowledged	/KEVIN QUINTO/ Examiner's Signature			TOTAL CLAIMS
				17
				INDEPENDENT CLAIMS
				3
ADDRESS FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER LLP 901 NEW YORK AVENUE, NW WASHINGTON, DC 20001-4413 UNITED STATES				
TITLE SEMICONDUCTOR MEMORY PREVENTING AN ELECTRIC SHORT CIRCUIT BETWEEN A WORD LINE AND A SEMICONDUCTOR SUBSTRATE, AND MANUFACTURING METHOD FOR THE SEMICONDUCTOR MEMORY				
FILING FEE RECEIVED 1224	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees	
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